

Patent

Old Attorney's Docket No. 040070-666

New Attorney's Docket No. 0119-146

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Allowed: May 9, 2005
Leif Magnus Andre NILSSON et al.) Group Art Unit: 2634
Application No.: 09/580,119) Examiner: ODOM, Curtis B
Filed: May 30, 2000) Confirmation No.: 1475
For: FRACTIONAL N PHASE LOCKED LOOP))

FACSIMILE COVER SHEET

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This Official Communication is being sent by facsimile transmission to the Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450 to the following facsimile number:

Facsimile Number: 703 746 4000 Date of Transmission: July 27, 2005

The Communication includes:

Issue Fee Transmittal Form (PTOL-85) (in duplicate);

Credit Card Payment Form (PTO-2038); and

Comments on Statement of Reasons for Allowance.

Total number of Pages (including this page): 6

I hereby certify that this correspondence is being sent by facsimile transmission to the Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450 to the following facsimile number:

Facsimile Number: 703 746 4000 Date of Transmission: July 27, 2005

Kenneth B. Leffler

SENDER INFORMATION

From: Kenneth B. Leffler

Voice Tel. No.: 703-718-8884



Patent
Old Attorney's Docket No. 040070-666
New Attorney's Docket No. 0119-146

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of) Allowed: May 9, 2005
Leif Magnus Andre NILSSON et al.) Group Art Unit: 2634
Application No.: 09/580,119) Examiner: ODOM, Curtis B
Filed: May 30, 2000) Confirmation No.: 1475
For: FRACTIONAL N PHASE LOCKED)

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants gratefully acknowledge the allowance of this application. These comments are presented to clarify the record of prosecution.

The Examiner's Statements of Reasons for Allowance refers in several places to the "phase detector" having certain features and comprising others. Applicants would like to point out that the various apparatus claims actually each define a "phase-locked loop". In some of these claimed embodiments (e.g., see independent apparatus claim 13), the phase-locked loop includes a phase detector as described by the first four lines of the Examiner's statement (i.e., having inputs of a reference signal and a comparison signal and generating a phase difference signal). However, contrary to the fourth and fifth lines of the Examiner's statement, the phase detector does not comprise the circuit that generates the comparison signal. Rather it is the phase-locked loop that comprises this element.

Application No.: 09/580,119
Old Attorney's Docket No. 040070-666
New Attorney's Docket No. 0119-146
Page 2

Furthermore, Applicants would like to point out that in other claimed embodiments (e.g., see independent apparatus claim 33), the phase detector generates a phase difference signal that represents a phase difference between the reference signal and a signal generated by the phase detector having twice the frequency of the comparison signal.

Respectfully submitted,
Potomac Patent Group PLLC

Date: July 27, 2005

Kenneth B. Leffler Registration No. 36,075

P.O. Box 855 McLean, Virginia 22101-0855 703-718-8884

I hereby certify that this correspondence is being sent by facsimile transmission to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 to the following

facsimile number:

Facsimile Number: 703 746 4000 Date of Transmission: July 27, 200

Kenneth B. Leffler